

The IEEE and ISTE Chapters of our institution jointly organized a technical seminar on **“Implementing E-Plan for Automated Electric Schematic Generation.”** The session was handled by **Er. K. V. Suganya** from **CAD Desk, Nagercoil**, who shared her expertise on modern electrical design automation tools.

The seminar focused on the use of **E-Plan software** for creating efficient, accurate, and automated electrical schematics. The resource person explained how automation in electrical documentation significantly reduces design time, minimizes errors, and enhances productivity in industrial and engineering applications.